

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Katsuyoshi MATSUURA, et al.

Serial No. 09/551,233

Filed: April 17, 2000

Title: SEMICONDUCTOR DEVICE HAVING A  
FERROELECTRIC CAPACITOR AND A  
FABRICATION PROCESS THEREOF



Examiner: H.-M. LEE

Art Unit: 2823

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.97, the Examiner may wish to consider the references listed on the attached PTO Form 1449. In submitting these references, no representation is made or implied that the references are or are not material to the examination of this application. The Examiner is encouraged to make his or her own determination of materiality. Copies of the references are provided.

This IDS is filed pursuant to 37 C.F.R. § 1.97(c)(1). The undersigned hereby states that each item of information contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to this filing. Accordingly, no fee is believed due. However, any fee deficiency may be charged to Deposit Account No. 50-1123.

Respectfully submitted,

April 17, 2000

A handwritten signature in cursive script, appearing to read "Carol W. Burton".

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Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The undersigned hereby certifies that the enclosed

1. Information Disclosure Statement;
2. Form 1449 with References;
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March 13, 2001

Maller

March 13, 2001

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